

	 SNUG Korea 2026 Agenda at a Glance				
08:00 - 09:00	Registration				
09:00 - 11:50	Opening Keynotes				
	환영사 시놉시스 코리아 소경신 대표이사				
	Synopsisys Keynote : Re-engineering the Future of Silicon Design by Shankar Krishnamoorthy, Chief Product Development Officer of Synopsisys Inc. - 45 minutes				
	Visionary Keynote : The Future of Agentic AI and Innovations in the HBM–HBF–HBS Memory Hierarchy KAIST 김정호 교수 - 30 minutes				
	Industry Keynote : Paving the way for Silicon Intelligence 삼성전자 류효겸 상무 - 25 minutes				
	Coffee Break - 20 minutes				
	Industry Keynote : Building a New Memory and Storage Hierarchy for AI Inference SK 하이닉스 정상규 부사장 - 25 minutes				
Visionary Keynote : From LLMs to Agentic and Physical AI: Where AI Models Are Headed and What They Demand from Silicon KAIST 신진우 교수- 20 minutes					
11:50 - 13:00	Lunch				
Track Title [Room]	AI-Powered Design Optimization [Grand Ballroom 2-3, 5F]	Multi-Die and Next-generation Memory [Grand Ballroom 1, 5F]	AI-Driven Silicon Innovation [Magnolia, 2F]	Scalable Verification for AI Chip Era [Orchid, 2F]	Shift-left Manufacturing Design [Rose, 5F]
13:00 - 13:30	Systematic Exploration of Design Variations using Parametric Scenario Extension (PSX) 정재원, Synopsisys	Multi-Die Prototyping, Bump Planning, and SI Analysis (with Demo) 임미선, Synopsisys	Using Synopsisys.ai Copilot in Fusion Compiler Platform 김수연, Synopsisys	Maximizing the benefits from Software-defined Hardware-assisted Verification 이강욱, Synopsisys	TCAD trend and update Kartik Venkataraman, Synopsisys
13:30 - 14:00	Achieving Signoff Confidence with IR-Aware STA 김두하, 삼성전자	The X-section optimization of 2.5D signal routing with 3DSO 김혜민, 삼성전자	Agentic AI & Analog, AI driven Analog Yuval Shay, Synopsisys	Pre-Silicon Real Workloads Validation : Integration of Wireless Network Testers on ZeBu 심규호, 노우영, 삼성전자	Overcoming DUV Patterning Challenges with ArF Immersion Extensions 김봉근, 삼성전자
14:00-14:30	AI-Driven Floorplan Exploration for Chip-Size Reduction with DSO.ai 김종협, 삼성전자	Scaling Timing Signoff for Advanced Node and Multi-Die Designs: Signoff Tutorial - PrimeClock 임철, Synopsisys	Analog Layout Synthesis: A new Paradigm to Accelerate Analog Design 김동희, Synopsisys	Reinforcement Learning–Based Optimization of SoC Performance Parameters on the ZeBu 박형태, 삼성전자	From Silicon to Systems: Synopsisys and NVIDIA Accelerated Engineering 윤기항, NVIDIA
14:30-15:00	Improving ATPG Debug Productivity Through Hold-Time Diagnosis 황도연, 삼성전자	Analysis of PDN in 12-High HBM 3DIC for Heterogeneous Integration 홍지우, 삼성전자	Chiplets for AI Data Centers: Shift-Left, 3DIC Integration, and Co-Packaged Optics Ravi Todi, Synopsisys	ZeBu - SpeedAdaptor3 solution for CXL 3.x memory 유민수, 삼성전자	TCAD application use-case 조남기, Synopsisys
15:00-15:20	Coffee Break				
15:20-15:50	Physics-Aware CNN-Based IR Drop Risk Prediction for Fast and Reliable SRAM Placement Optimization 장세형, SK 하이닉스	Thermal Analysis Workflow Introduction from On-Chip to PKG 최도수, 박상재, ADTechnology	Solving the AI Compute-to-I/O Gap with Scalable Interface IP Manmeet Walia, Synopsisys	VC 201X - Manufacturing Fault Simulation 이성욱, 삼성전자	Invited talk : Physics-Informed Graph Neural Surrogate for TCAD 김성윤, Synopsisys with SK 하이닉스
15:50-16:20	Low Power Optimization through Fast and Accurate Time-Based Power Analysis 박소라, 삼성전자	ML-Based Sigma Av–aware Switch Cell Optimization 변성수, 삼성전자	Addressing Physical AI Application Challenges with Standards-Based IP Mehak Kalra, Synopsisys	Technology Overview: Advancing Analog, Mixed-signal, and RF Designs Yuval Shay, Synopsisys	Computational Lithography for the AI Era Al Blais, Synopsisys
16:20-16:50	Product test pattern count reduction from ATPG configuration optimization with TSO.ai 박재석, 삼성전자	Accelerating Signoff TAT with HyperScale/HyperGrid and PrimeClosure 오덕근, 삼성전자	Advanced PVT Monitoring Solutions for N2P GAA Technology and AI Workloads 한세희, Synopsisys	Shift-Left Digital Twin with Virtual Prototyping 장인광, Synopsisys	Invited talk : Interface physics and contact engineering for 2D FETs using QuantumATK 정창욱, UNIST
16:50-17:10	Lucky draw & Closing				
*발표자 사정에 의해 일정이 변경될 수 있습니다.					